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new and updated material on GaN-based and quantum-dot lasers, photonic IC technology, detectors, modulators and SOAs, DVDs and storage, eye diagrams and BER concepts, and DFB lasers. Appendices will also be expanded to include quantum-dot issues and more on the relation between spontaneous emission and gain.

The latest techniques for designing robust, high performance integrated circuits in nanoscale technologies Focusing on a new technological paradigm, this practical guide describes the interconnect-centric design methodologies that are now the major focus of nanoscale integrated circuits (ICs). High Performance Integrated Circuit Design begins by discussing the dominant role of on-chip interconnects and provides an overview of technology scaling. The book goes on to cover data signaling, power management, synchronization, and substrate-aware design. Specific design constraints and methodologies unique to each type of interconnect are addressed. This comprehensive volume also explains the design of specialized circuits such as tapered buffers and repeaters for data signaling, voltage regulators for power management, and phase-locked loops for synchronization. This is an invaluable resource for students, researchers, and engineers working in the area of high performance ICs. Coverage includes: Technology scaling Interconnect modeling and extraction Signal propagation and delay analysis Interconnect coupling noise Global signaling Power generation Power distribution networks CAD of power networks Techniques to reduce power supply noise Power dissipation Synchronization theory and tradeoffs Synchronous system characteristics On-chip clock generation and distribution Substrate noise in mixed-signal ICs Techniques to reduce substrate noise

The monograph begins with a systematic introduction of chaos and chaos synchronization, and then extends to the methodologies and technologies in secure communication system design and implementation. The author combines theoretical frameworks with

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empirical studies, making the book a practical reference for both academics and industrial engineers.

Masters Theses in the Pure and Applied Sciences was first conceived, published, and disseminated by the Center for Information and Numerical Data Analysis and Synthesis (CINDAS) \* at Purdue University in 1957, starting its coverage of theses with the academic year 1955. Beginning with Volume 13, the printing and dissemination phases of the activity were transferred to University Microfilms/Xerox of Ann Arbor, Michigan, with the thought that such an arrangement would be more beneficial to the academic and general scientific and technical community. After five years of this joint undertaking we had concluded that it was in the interest of all concerned if the printing and distribution of the volume were handled by an international publishing house to assure improved service and broader dissemination. Hence, starting with Volume 18, Masters Theses in the Pure and Applied Sciences has been disseminated on a worldwide basis by Plenum Publishing Corporation of New York, and in the same year the coverage was broadened to include Canadian universities. All back issues can also be ordered from Plenum. We have reported in Volume 25 (thesis year 1980) a total of 10,308 theses titles from 27 Canadian and 214 United States universities. We are sure that this broader base for theses titles reported will greatly enhance the value of this important annual reference work. While Volume 25 reports theses submitted in 1980, on occasion, certain universities do report theses submitted in previous years but not reported at the time.

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Now revised with a stronger emphasis on applications and more problems, this new Fourth Edition gives readers the opportunity to analyze, design, and evaluate linear circuits right from the start. The book's abundance of design examples, problems, and applications, promote creative skills and show how to choose the best design from several competing solutions. \* Laplace first. The text's early introduction to Laplace transforms saves time spent on transitional circuit analysis techniques that will be superseded later on. Laplace transforms are used to explain all of the important dynamic circuit concepts, such as zero state and zero-input responses, impulse and step responses, convolution, frequency response, and Bode plots, and analog filter design. This approach provides students with a solid foundation for follow-up courses.

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