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EXERCISE SOLUTIONS INTRODUCTION 1 1.2 Three definitions of "bit": (1) A binary digit (p. 1). (2) Past tense of "bite" (p. 1). (3) A small amount (pp. 6, 10). 1.3 ASIC Application-Specific Integrated Circuit CAD Computer-Aided Design CD Compact Disc CO Central Office CPLD Complex Programmable Logic Device DAT Digital Audio Tape

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This book takes an authoritative introduction to basic principles of digital design and practical requirements in both board-level and VLSI systems. Digital Design covers the most widespread logic design practices while building a solid foundation of theoretical and engineering principles. This easy-to-follow book uses a practical writing style. Includes low voltage and LVCMOS/LVTTL. Coverage of Complex Programmable Logic Devices (CPLDs) and Field-Programmable Gate Arrays (FPGAs). Introduction of HDL-based digital design Covers VHDL as well as ABEL. Including simulation and synthesis.

With over 30 years of experience in both industrial and university settings, the author covers the most widespread logic design practices while building a solid foundation of theoretical and engineering principles for students to use as they go forward in this fast moving field.

Establishing a solid foundation of digital design principles An authoritative introduction to basic digital design, Digital Design: Principles and Practices helps readers build a foundational understanding of theoretical and engineering principles. This book gives readers the opportunity to learn the basics at the high level (HDLs), at the low level (electrical circuits), and throughout the "vast middle" (gates, flip-flops, and higher-level digital-design building blocks). The author's 30 years of experience in both industrial and university settings brings weight and credibility to the material, and with broad coverage of logic design practices, the 5th Edition gives readers a look at how digital design works in the real world.

With over 30 years of experience in both industrial and university settings, the author covers the most widespread logic design practices while building a solid foundation of theoretical and engineering principles for students to use as they go forward in this fast moving field.

An eagerly anticipated, up-to-date guide to essential digital design fundamentals Offering a modern, updated approach to digital design, this much-needed book reviews basic design fundamentals before diving into specific details of design optimization. You begin with an examination of the low-levels of design, noting a clear distinction between design and gate-level minimization. The author then progresses to

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the key uses of digital design today, and how it is used to build high-performance alternatives to software. Offers a fresh, up-to-date approach to digital design, whereas most literature available is sorely outdated Progresses though low levels of design, making a clear distinction between design and gate-level minimization Addresses the various uses of digital design today Enables you to gain a clearer understanding of applying digital design to your life With this book by your side, you'll gain a better understanding of how to apply the material in the book to real-world scenarios.

The renowned communications theorist Robert Gallager brings his lucid writing style to the study of the fundamental system aspects of digital communication for a one-semester course for graduate students. With the clarity and insight that have characterized his teaching and earlier textbooks, he develops a simple framework and then combines this with careful proofs to help the reader understand modern systems and simplified models in an intuitive yet precise way. A strong narrative and links between theory and practice reinforce this concise, practical presentation. The book begins with data compression for arbitrary sources. Gallager then describes how to modulate the resulting binary data for transmission over wires, cables, optical fibers, and wireless channels. Analysis and intuitive interpretations are developed for channel noise models, followed by coverage of the principles of detection, coding, and decoding. The various concepts covered are brought together in a description of wireless communication, using CDMA as a case study.

This popular volume provides a solid foundation in the elements of basic digital electronics and switching theory that are used in most practical digital design today -- and builds on that theory with discussions of real-world digital components, design methodologies, and tools. Covers a full range of topics -- number systems and codes, digital circuits, combinational logic design principles and practices, combinational logic design with PLDs, sequential logic design principles and practices, sequential logic design with PLDs, memory, and additional real-world topics (e.g., computer-aided engineering tools, design for testability, estimating digital system reliability, and transmission lines, reflections, and termination). This edition introduces PLDs as soon as possible, emphasizes CMOS logic families and introduces digital circuits in a strongly technology-independent fashion, covers the latest Generic Array Logic (GAL) devices, offers expanded coverage of ROM and RAM system-level design, and provides additional design examples. For those needing a solid introduction or review of the principles and practices of modern digital design. Previously announced in Oct. 1992 PTR Catalogue.

Explores the unique hardware programmability of FPGA-based embedded systems, using a learn-by-doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC (system on a programmable chip) integrates a processor, memory modules, I/O peripherals, and custom hardware accelerators into a single FPGA (field-programmable gate array) device. In addition to the customized software, customized hardware can be developed and incorporated into the embedded system as well—allowing us to configure the soft-core processor, create tailored I/O interfaces, and develop specialized hardware accelerators for computation-intensive tasks. Utilizing an Altera FPGA prototyping board and its Nios II soft-core processor, Embedded SoPC Design with Nios II Processor and Verilog Examples takes a "learn by doing" approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board. Emphasizing hardware design and integration throughout, the book is divided into four major parts: Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an

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overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I/O peripherals, including a PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD (secure digital) card Part IV provides several case studies of the integration of hardware accelerators, including a custom GCD (greatest common divisor) circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology While designing and developing an embedded SoPC can be rewarding, the learning can be a long and winding journey. This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology.

For sophomore courses on digital design in an Electrical Engineering, Computer Engineering, or Computer Science department. & Digital Design, fourth edition is a modern update of the classic authoritative text on digital design.& This book teaches the basic concepts of digital design in a clear, accessible manner. The book presents the basic tools for the design of digital circuits and provides procedures suitable for a variety of digital applications.

This book is designed to facilitate a thorough understanding of the fundamental principles of design without requiring readers to memorize an excess of confusing technological details. It integrates fundamentals with state-of-the-art techniques in computer design to demonstrate the complete design process, from specification to manufacturing. FEATURES: Presents clear connections between principles and practice. (Throughout) Progresses naturally and patiently through the design process, ranging in complexity from logic and sequential levels to the levels of RISC processors and complete ASICs. (Throughout) Introduces a generic component library that reflects practical design constraints to help explain concepts and implement worked-out examples. (Ch. 5, 7) Takes a contemporary approach to logic and sequential design, emphasizing a coherent design process instead of manual design techniques. (Throughout) Introduces an ASIC design process based on the sequential and behavioral synthesis used in modern CAD tools. (Ch. 8) De-mystifies the art of processor design by extending synthesis techniques to microprocessor design. (Ch. 9) Demonstrates processor design on CISC and RISC processors including instruction set design and datapath design with data-forwarding and branch prediction. (Ch. 9) The book features: Step-by-step design procedures in each chapter. Comprehensive worked examples that demonstrate designer's options and choices. (Throughout) Over 300 color illustrations that use color to enhance learning and material retention.

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